

Clocking Options

A special custom clocking program is loaded into the module every time you load one of the SPI3_TX, SPI3_RX, SPI4, and SPI4_LVTTL support packages from the TCS101 product. Each support package offers different clocking options. You may use the default clocking option or choose an alternate by clicking the “More...” button in the logic analyzer setup window.

SPI3_TX and SPI3_RX

The software provides two custom clocking options for the SPI3_TX and SPI3_RX support packages.

Cycles. The Cycles option provides the following choices:

- “All” is for storing data on every clock cycle (default).
- “Active Only” is for storing data only when the data bus is valid.

Physical Port. The Physical Port option provides the following choices:

- “Single-No InBand Addr” is for a single physical port without TSX or RSX signals (default).
- “Single With InBand Addr” is for a single physical port with TSX or RSX signals.
- “Multiple” is for multiple physical port interfaces. This option uses TSX or RSX and PTPA signals, including byte level and packet level transfer modes.

SPI4 and SPI4_LVTTL

The software provides one custom clocking option for SPI4 and SPI4_LVTTL support packages:

All Cycles. “All Cycles” is for storing data on every clock cycle.

Setup/Hold Time Adjustment

Some devices you test may require an adjustment of the setup/hold values in the TLA700 Application. For more information, refer to the section *SPI-4.2 Setup/Hold Time Adjustments* on page 2–17.